

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L7	6948 (programmable adj logic adj (device array)) fpga pla pldasic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:42	
2	BRS	L8	2148 1 and interconnect\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:42	
3	BRS	L9	5242 7 and ((driver buffer repeat\$r) same (delay\$3 stagger\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:43	
4	BRS	L10	2848 9 and interconnect\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:43	
5	BRS	L12	254 10 and stagger\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/06 15:46	